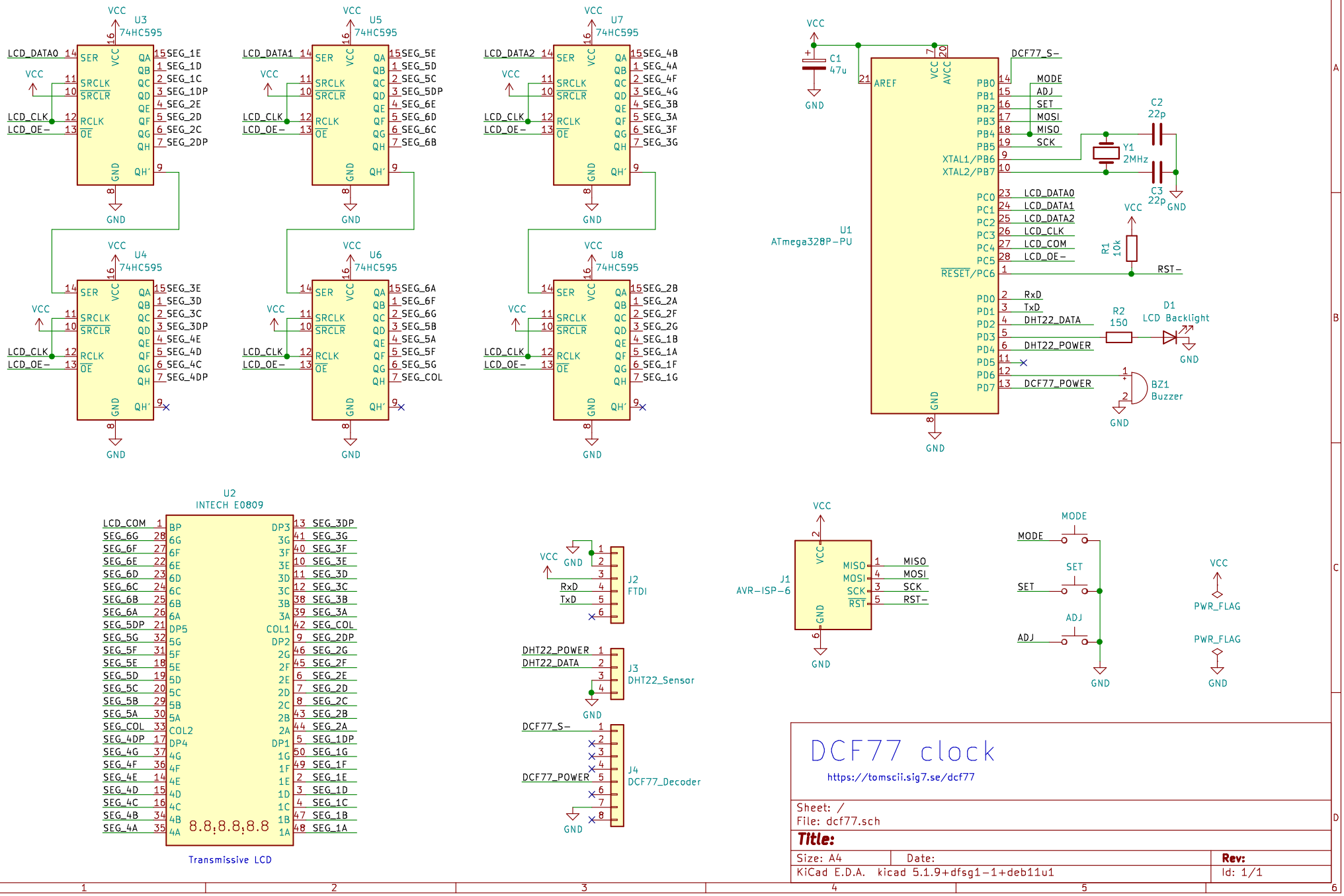
U3
74HC595
LCD_DATA0
LCD_CLK
LCD_OE−
SEG_1E
SEG_1D
SEG_1C
SEG_1DP
SEG_2E
SEG_2D
SEG_2C
SEG_2DP
U4
74HC595
SEG_3E
SEG_3D
SEG_3C
SEG_3DP
SEG_4E
SEG_4D
SEG_4C
SEG_4DP
U5
74HC595
LCD_DATA1
SEG_5E
SEG_5D
SEG_5C
SEG_5DP
SEG_6E
SEG_6D
SEG_6C
SEG_6B
U6
74HC595
SEG_6A
SEG_6F
SEG_6G
SEG_5B
SEG_5A
SEG_5F
SEG_5G
SEG_COL
U7
74HC595
LCD_DATA2
SEG_4B
SEG_4A
SEG_4F
SEG_4G
SEG_3B
SEG_3A
SEG_3F
SEG_3G
U8
74HC595
SEG_2B
SEG_2A
SEG_2F
SEG_2G
SEG_1B
SEG_1A
SEG_1F
SEG_1G
U1
ATmega328P−PU
C1
47u
C2
22p
C3
22p
Y1
2MHz
R1
10k
R2
150
D1
LCD Backlight
BZ1
Buzzer
RST−
RxD
TxD
DHT22_DATA
DHT22_POWER
DCF77_S−
MODE
ADJ
SET
MISO
SCK
LCD_COM
LCD_DATA0
LCD_DATA1
LCD_DATA2
LCD_CLK
LCD_COM
LCD_OE−
U2
INTECH E0809
Transmissive LCD
J1
AVR−ISP−6
J2
FTDI
J3
DHT22_Sensor
J4
DCF77_Decoder
PWR_FLAG
VCC
GND
DCF77 clock
https://tomscii.sig7.se/dcf77
Sheet: /
File: dcf77.sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad 5.1.9+dfsg1−1+deb11u1
Id: 1/1